

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLN. OF: YOSHINO
FILED: November 17, 2003
FOR: Forming Method of Resist Pattern for Improving Accuracy of Dimension
of Pattern, Manufacturing Method of Semiconductor Apparatus Using the
Same and Heat Treatment Apparatus for the Same
DOCKET: ELPIDA 03USFP934

MAIL STOP PATENT APPLICATION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

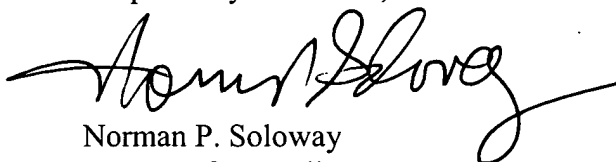
Dear Sir:

In connection with the above-entitled matter, Applicant hereby attaches U.S. Patent Office Form PTO-1449, including copies of the references listed therein, which were considered prior to the initial filing, and are discussed in the specification. The claims in the present application are believed to be patentably distinguished over these references.

This information disclosure statement is being made pursuant to the duty of disclosure imposed by law and formulated in 37 CFR 1.56(A). No representation is made that the information thus disclosed in fact constitutes prior art or that it is the closest prior art, inasmuch as 37 CFR 1.56(A) relies on a materiality concept which depends on subjectivity.

In the event there are any fee deficiencies or additional fees are payable, please charge them (or credit any overpayment) to our Deposit Account No. 08-1391.

Respectfully submitted,



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I hereby certify that this paper and the papers listed thereon are being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to MAIL STOP PATENT APPLICATION, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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ELPIDA 03USFP934

YOSHINO

November 17, 2003

GROUP

[illegible]

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
		JP 11-295904	29.10.1999	JAPAN (w/Abstract)	G03F	7/40		✓
		JP 11-119443	30.04.1999	JAPAN (w/Abstract)	G03F	7/40		✓
		JP 10-274854	13.10.1998	JAPAN (w/Abstract)	G03F	7/40		✓
		JP 09-190871	22.07.1997	JAPAN (w/Abstract)	H05B	3/00		✓
		JP 11-008180	12.01.1999	JAPAN (w/Abstract)	H01L	21/027		✓

DATE CONSIDERED

PAGE 1 OF 2

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APPLICANT(S)

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U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
	JP 10-055951	24.02.1998	JAPAN (w/Abstract)	H01L	21/027		✓
	2002-064047	28.02.2002	JAPAN (w/Abstract)	H01L	21/027		

OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, Etc.)*

EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.